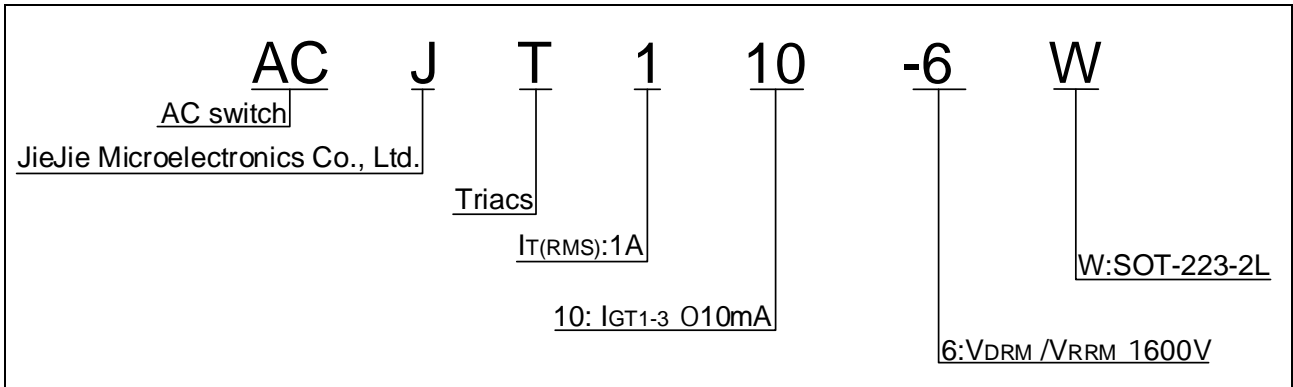


JIEJIE MICRO(M)-2.4 (I)-4 vM

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MARKING

FIG.1: Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger current,

FIG.8 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

e	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package
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DELIVERY MODE

