

**JIEJIE MICROELECTRONICS CO., LTD.**

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# JR0205H

Peak gate power	$P_{GM}$	2	W
Peak pulse voltage ( $T_j=25$ ; non-repetitive,off-state;FIG.7)	$V_{pp}$	0.5	kV

**NOTE 1:** Operating junction temperature  $T_j$  is up to 125 when a resistor 1k is connected between Gate and Cathode. Without this resistor, the  $T_j$  is up to 110 only.

1k

( $T_j=25$  unless otherwise specified)

Symbol	Test Condition	Value			Unit
		MIN.	TYP.	M	
					2DC 75: $T_j$

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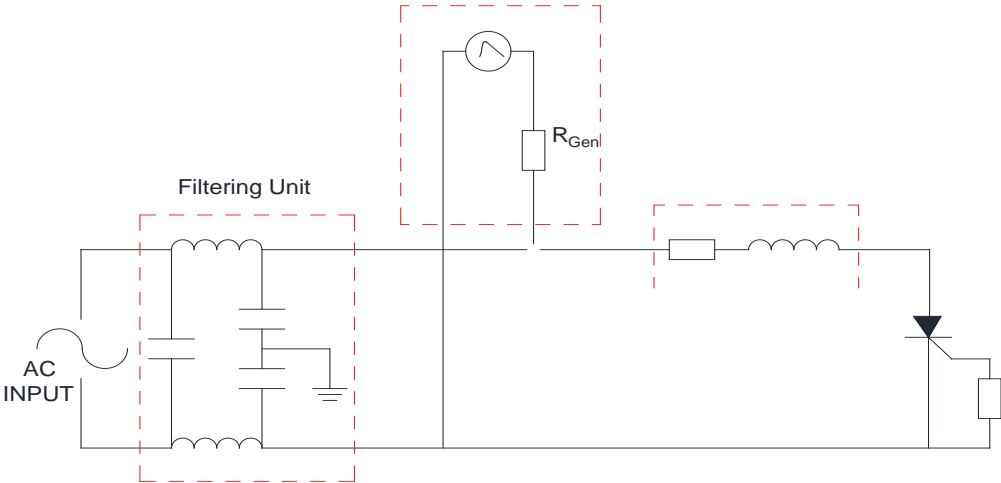
**FIG.1:** Maximum power dissipation versus RMS on-state current

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**FIG.2:** RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards


IEC61000-4-5 Standards  
Surge Generator





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