



The ACJP01U-800SW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications





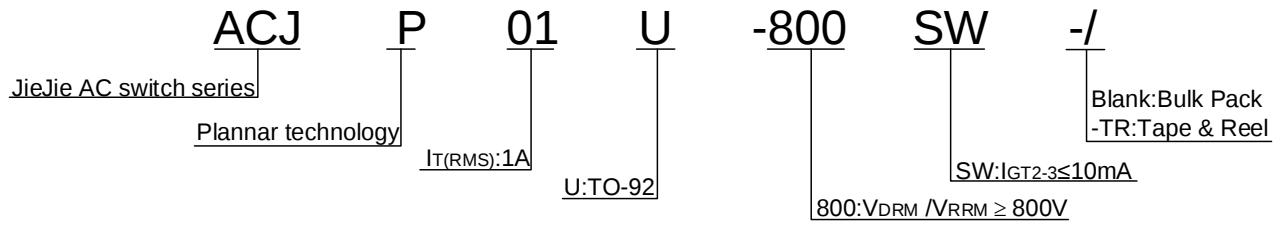
Average gate power dissipation ($T_j=125$)	$P_{G(AV)}$	0.1	W
Peak gate power	P_{GM}	2	W
Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG. 7)	V_{pp}	4.5	kV

($T_j=25$ unless otherwise specified)

I_{GT}	$V_D=12V$ $R_L=33\Omega$	-	MAX.	10	mA
V_{GT}		-	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k\Omega$	-	MIN.	0.15	V
I_L	$I_G=1.2I_{GT}$		MAX.	25	mA
				10	
I_H	$I_T=100mA$		MAX.	10	mA
dV/dt	$V_D=540V$ Gate Open $T_j=125$		MIN.	100	V/ μs
$(dI/dt)_c$	$(dV/dt)_c=10V/\mu s$, $T_j=125$		MIN.	0.6	A/ms
t_{on}	$I_G=20mA$ $I_A=200mA$ $I_R=20mA$ $T_j=25$		TYP.	2.5	μs
t_{off}				25	
V_{CL}	$I_{CL}=0.1mA$ $t_p=1ms$		MIN.	900	V

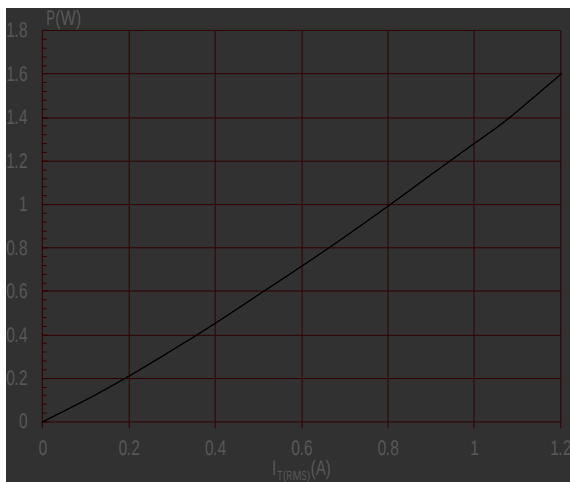
V_{TM}	$I_{TM}=1.1A$ $t_p=380\mu s$	$T_j=25$	1.3	V
V_{TO}	Threshold voltage	$T_j=125$	0.77	V
R_D	Dynamic resistance	$T_j=125$	276	$m\Omega$
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25$	5	μA
I_{RRM}		$T_j=125$	0.4	mA

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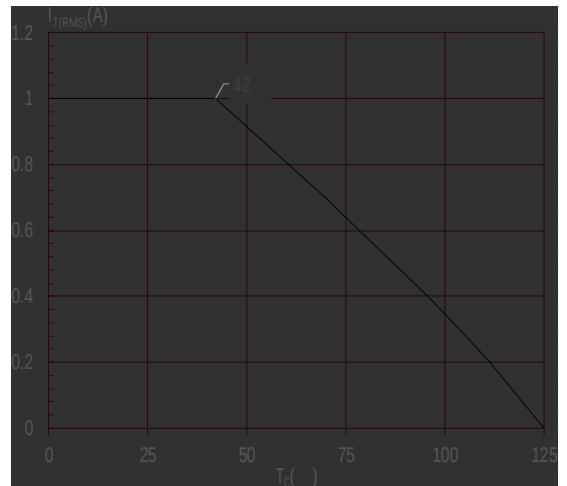




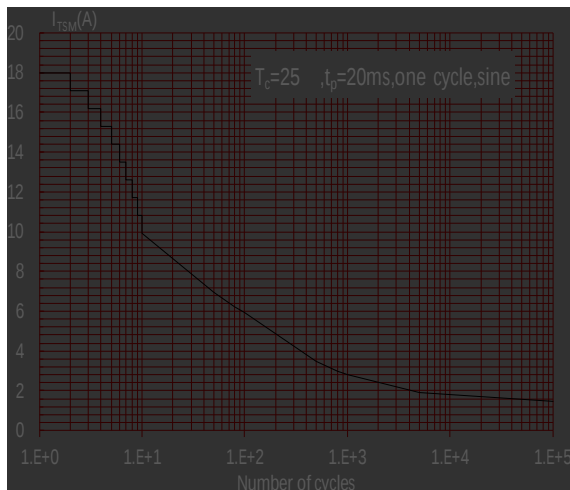
Maximum power dissipation versus RMS on-state current



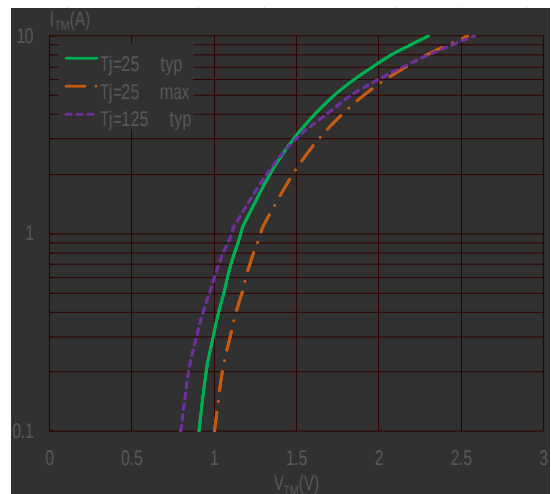
RMS on-state current versus case temperature



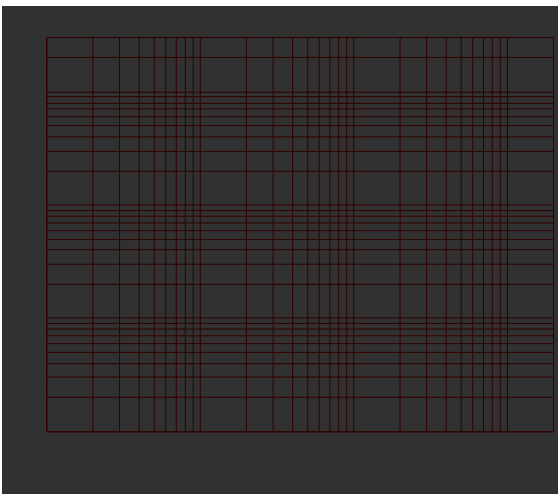
Surge peak on-state current versus number of cycles



On-state characteristics



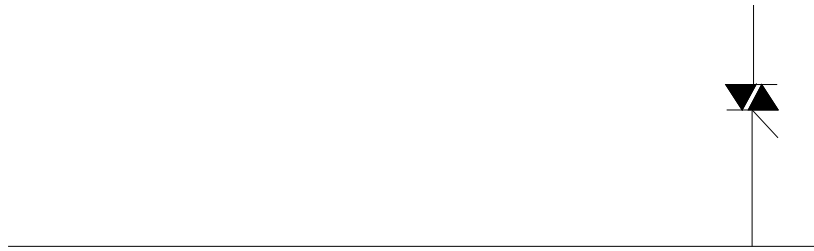
Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20ms$, and corresponding value of I^2t ($dI/dt < 100A/\mu s$)



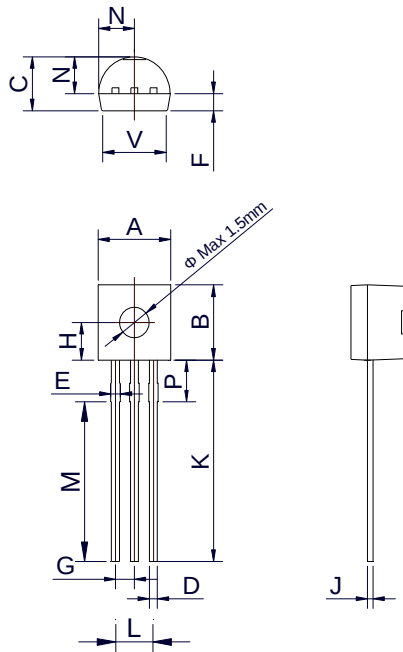
Relative variations of gate trigger current, holding current and latching current versus junction temperature



FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards







Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.45		5.20	0.175		0.205
B	4.32		5.33	0.170		0.210
C	3.18		4.19	0.125		0.165
D	0.407		0.533	0.016		0.021
E	0.50		0.70	0.020		0.028
F	1.10		1.30	0.043		0.051
G	1.10		1.40	0.043		0.055
H	2.20		2.40	0.087		0.094
J	0.36		0.50	0.014		0.020
K	12.70		15.0	0.500		0.591
L	2.44		2.64	0.096		0.104
M	11.64		12.04	0.458		
N	2.04		2.66	0.080		0.105
P	1.80		2.30	0.071		0.091
V	4.10		4.50	0.161		0.177





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