

ORDERING INFORMATION

<u>J</u>	<u>ST</u>	<u>10</u>	<u>C</u>	<u>-800</u>	<u>BW</u>
JieJie Microelectronics Co., Ltd.	Triacs	$I_{T(RMS)}:10A$	$C:TO-220C$	$800:V_{DRM}/V_{RRM} \geq 800V$	$BW:I_{GT1-3} \leq 50mA$

MARKING

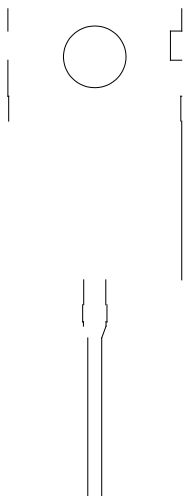


FIG.1: Maximum power dissipation versus RMS on-state current

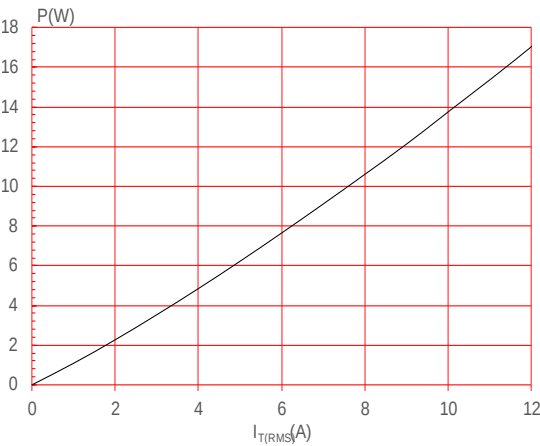


FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

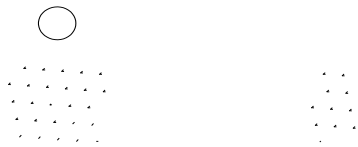
ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST10C-800BW	800	50	TO-220C	50	Tube

Document Revision History

Date	Revision	Changes
Apr.11, 2023	A.1.0	Last updated
Oct.15, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

PACKAGE MECHANICAL DATA



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.