



JST12A-800B 12A TRIAC

Rev.A.1.1

DESCRIPTION:

The JST12A-1 0 Td [1 1 Tf 0 Tc.4911 Td[(h0)-0.6 (0B)]TJc 0.01340 Tw 10 0 Td(-1 0 Td [1 1 TJ

Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	2	kV
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ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V$ $R_L=33\Omega$	- -	MAX.	50	mA
				70	
V_{GT}		ALL	MAX.	1	V
V_{GD}					

ORDERING INFORMATION

J	ST	12	A	-800	B
JieJie Microelectronics Co., Ltd.	Triacs	$I_{T(RMS)}:12A$	A:TO-220A(Ins)	$800:V_{DRM}/V_{RRM} \geq 800V$	B: $I_{GT1-3} \leq 50mA$ $I_{GT4} \leq 70mA$

MARKING

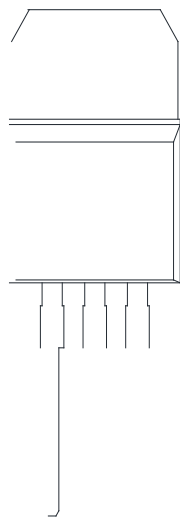


FIG.1: Maximum power dissipation versus RMS on-state current



FIG.2: RMS on-state current versus case temperature

ORDERING INFORMATION

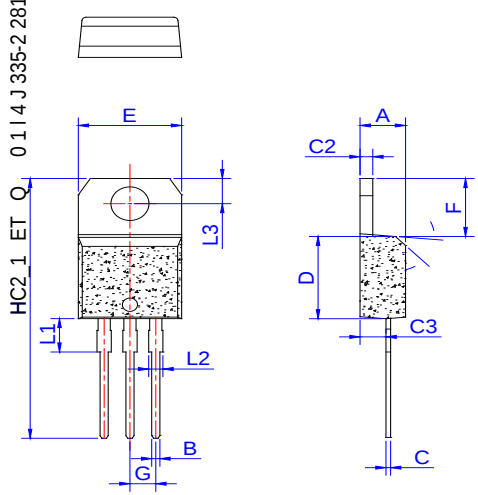
Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		- -				
JST12A-800B	800	50	70	TO-220A(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Oct.13, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

JST12A-800B
0 114 J 335-2 281 5800 2164 8 1696 J 3113

PACKAGE MECHANICAL DATA



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