

# JIEJIE MICROELECTRONICS CO., LTD.

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JST12E-600SW 12A TRIAC

Rev.A.1.1

DESCRIPT 6w 0.41E0.7 -A gil~pS...i 8V `pÖU À p@!InÈ3R p " D†H(d 0 à À





FIG.1: Maximum power dissipation versus RMS on-state current

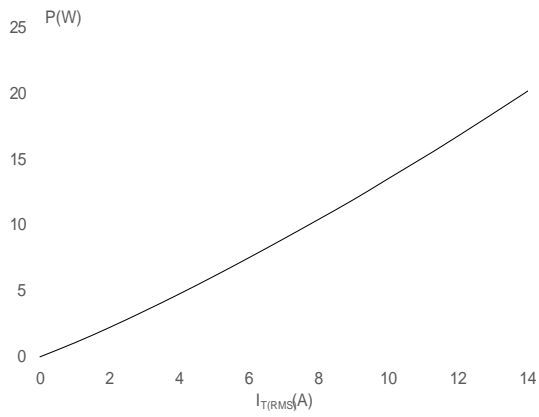


FIG.3: RMS on-state current versus ambient temperature (printed circuit board FR4,copper WKLFNQHVVP IXOO F\FOH)

FIG.2: RMS on-state current versus case temperature

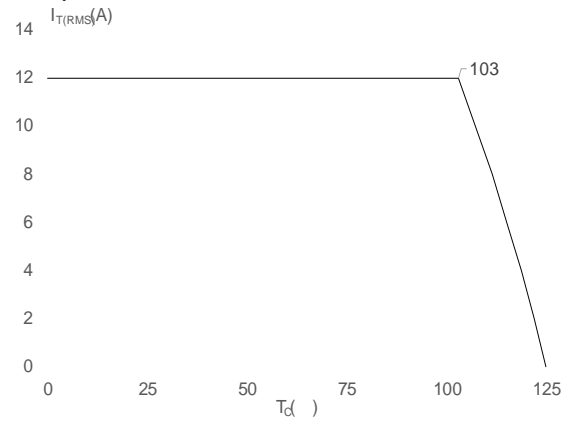


FIG.4: Surge peak on-state current versus number of cycles

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

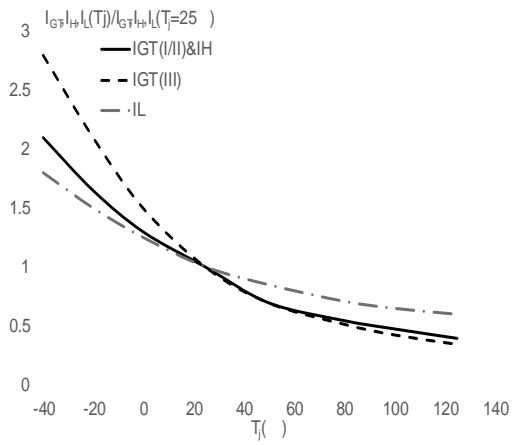


FIG.8 Test circuit for inductive and resistive loads to IEC-61000

## ORDERING INFORMATION

| Order code      | Voltage<br>$V_{DRM}/V_{RRM}$ (V) | IGT(mA) | Package | Base qty.<br>(pcs) | Delivery mode |
|-----------------|----------------------------------|---------|---------|--------------------|---------------|
|                 |                                  | - -     |         |                    |               |
| JST12E-600SW    | 600                              | 10      | TO-263  | 50                 | Tube          |
| JST12E-600SW-TR |                                  |         |         | 800                | Tape & Reel   |

## Document Revision History

| Date         | Revision | Changes                        |
|--------------|----------|--------------------------------|
| Apr.11, 2023 | A.1.0    | Last updated                   |
| Oct.20, 2025 | A.1.1    | Revise PACKAGE MECHANICAL DATA |





