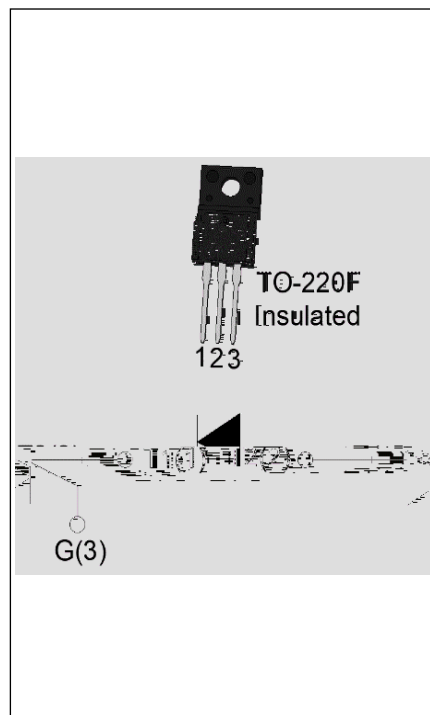




The JST12F-800B triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. By using an external plastic package, JST12F-800B provides a rated insulation voltage of 2000 VRMS, complying with UL standards (File ref: E252906). Package TO-220F is RoHS compliant.

$I_{T(RMS)}$	12	A
V_{DRM}/V_{RRM}	800	V
$I_{GT} / / /$	50/50/50/70	mA



Parameter		Symbol	Value	Unit
Storage junction temperature range		T _{stg}	-40-150	
Operating junction temperature range		T _j	-40-125	
Repetitive peak off-state voltage (T _j =25 °C)		V _{DRM}	800	V
Repetitive peak reverse voltage (T _j =25 °C)		V _{RRM}	800	V
RMS on-state current (T _C 085 °C)		I _{T(RMS)}	12	A
Non repetitive surge peak on-state current (full cycle , t _p =20ms , T _j =25 °C)		I _{TSM}	120	A
Non repetitive surge peak on-state current (full cycle , t _p =16.6ms , T _j =25 °C)			132	
I ² t value for fusing (t _p =10ms , T _j =25 °C)		I ² t	72	A ² s
Critical rate of rise of on-state current (I _G =2 hI _{GT} , f=100Hz , T _j =125 °C)	-	di/dt	100	A/μs
	-		50	
Peak gate current (t _p =20μs , T _j =125 °C)		I _{GM}	4	A
Average gate power dissipation (T _j =125 °C)		P _{G(AV)}	0.5	W
Peak gate power		P _{GM}	10	W



Peak pulse voltage
($T_j=25$



FIG.1: Maximum power dissipation versus RMS on-state current

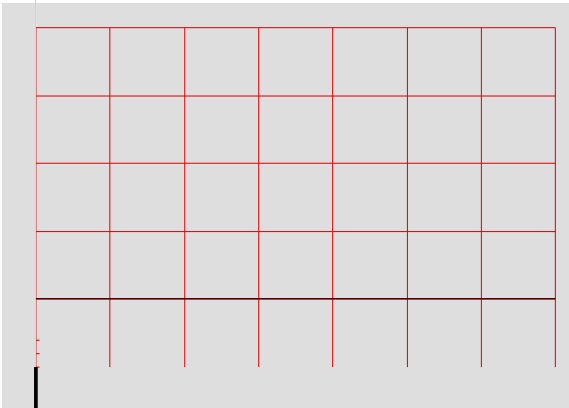
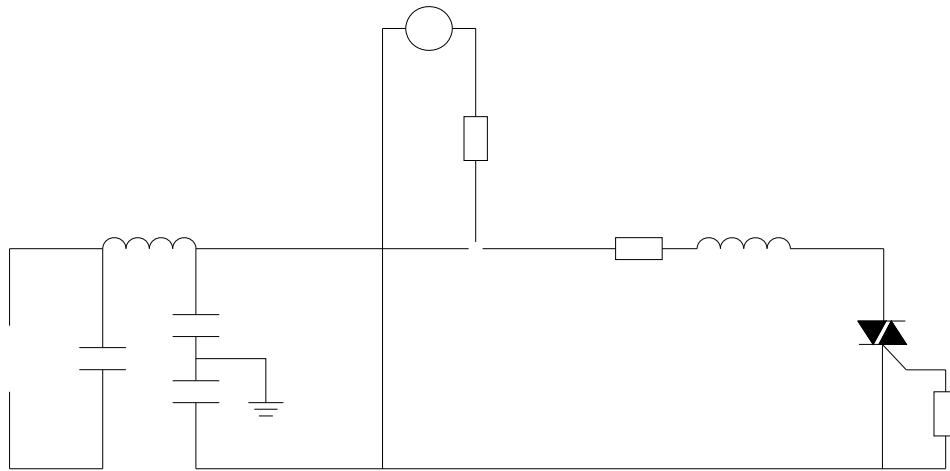


FIG.2: RMS on-state current versus case temperature



FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards





Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.



is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright © 2025 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.