

ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V$ $R_L=33\Omega$	ALL	MAX.	5	mA
V_{GT}		ALL	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k\Omega$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	- -	MAX.	9	mA
				13	
I_H	$I_T=100mA$		MAX.	5	mA
dV/dt	$V_D=540V$ Gate Open $T_j=110$		MIN.	20	V/ μs
$(dV/dt)_c$	$(dI/dt)_c=1.8A/ms$, $T_j=110$		MIN.	1.2	V/ μs
t_{on}	$I_G=10mA$ $I_A=200mA$ $I_R=20mA$ $T_j=25$		TYP.	2	μs
t_{off}				20	

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=5A$ $t_p=380\mu s$	$T_j=25$	1.55	V
V_{TO}	Threshold voltage	$T_j=125$	0.92	V
R_D	Dynamic resistance	$T_j=125$	107	m Ω
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25$	5	μA
I_{RRM}		$T_j=125$	0.35	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	7.0	/W
$R_{th(j-a)}$	junction to ambient (AC)	150	/W

ORDERING INFORMATION

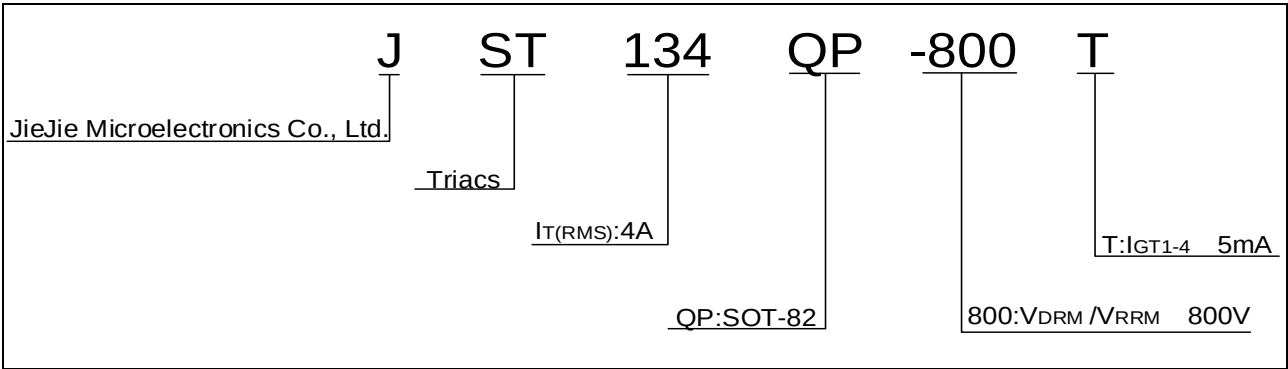


FIG.1: Maximum power dissipation versus RMS on-state current

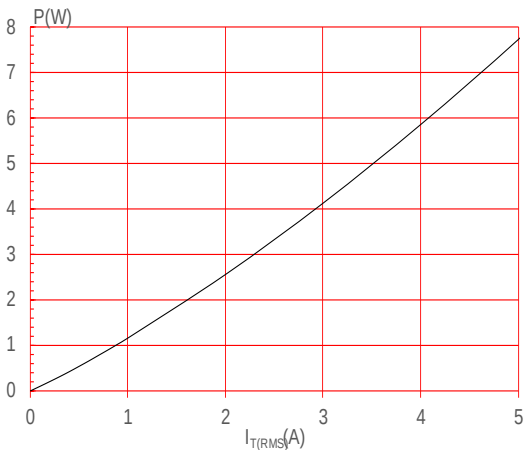


FIG.3: Surge peak on-state current versus number of cycles

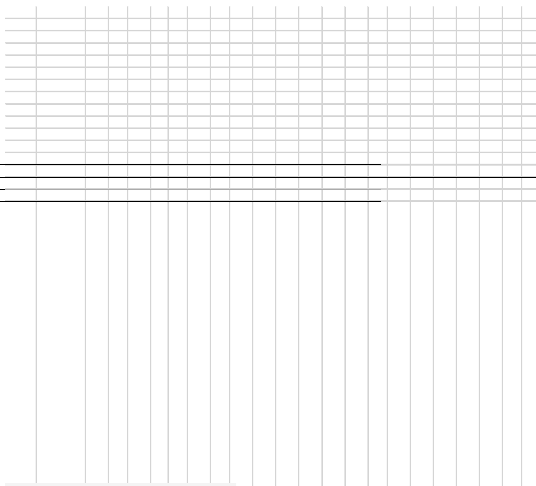


FIG.2: RMS on-state current versus case temperature

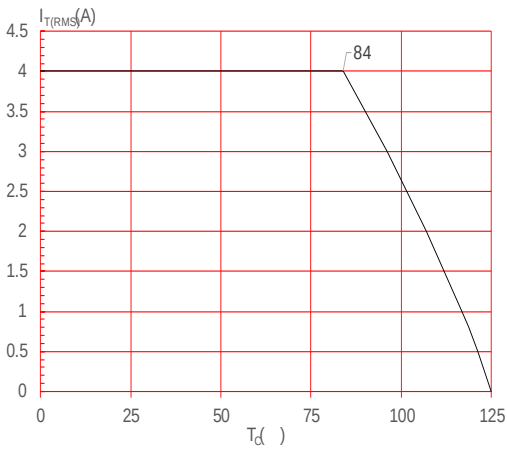
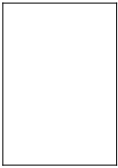


FIG.4: On-state characteristics

ORDERING INFORMATION

Order code

PACKAGE MECHANICAL DATA^(PCS)



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