

**JST80IS-1200BW 80A TRIAC**

Rev.A.1.1

The JST80IS-1200BW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST80IS-1200BW snubberless triac is especially recommended for use on inductive loads. By using an internal ceramic pad, JST80IS-1200BW provides a rated insulation voltage of 2500 VRMS, complying with UL standards (File ref: E252906). Package ITO-247 is RoHS compliant.

Symbol	Value	Unit
$I_{T(RMS)}$	80	A
V_{DRM}/V_{RRM}	1200	

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	1200	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	1200	V
RMS on-state current ($T_c=90^\circ\text{C}$)	$I_{T(RMS)}$	80	A
Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	800	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		880	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	3200	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}$, $f=100\text{Hz}$, $T_j=125^\circ\text{C}$)	di/dt	100	$\text{A}/\mu\text{s}$
Peak gate current ($t_p=20\text{ }\mu\text{s}$, $T_j=125^\circ\text{C}$)	I_{GM}	10	A

JST80IS-1200BW

Average gate power dissipation ($T_j=125$)	$P_{G(AV)}$	0.5	W
Peak gate power Peak pulse voltage ($T_j=25$)	P_{GM}	25	W

FIG.7 Test circuit for inductive and resistivs tEC

-

Order code	Voltage $V_{DRM}/V_{RRM}(V)$	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST80IS-1200BW	1200	50	ITO-247(Ins)	25	Tube

Document Revision History

Date	Revision	Changes
------	----------	---------

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.



is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright © 2025 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.